

# HESSI

High Energy Solar Spectroscopic Imager



SPECTRUMASTRO

## TRAINING

### SPACECRAFT THERMAL CONTROL SUBSYSTEM

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### Topics

- Component Layout
- Thermal Design Overview
- Thermal Hardware Summary
- Operational Considerations/Constraints
- Operational Contingencies
- Summary
- Appendix A - Pictures of Heaters, Thermostats and Temperature Sensors
- Appendix B - Typical Temperature Profiles
  - Model Predicts
  - TVAC Test Data

# HESSI

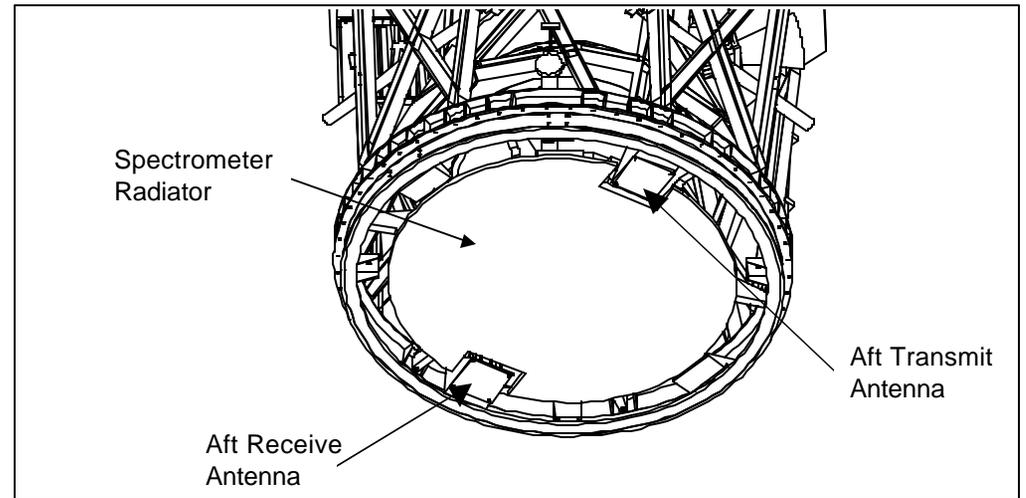
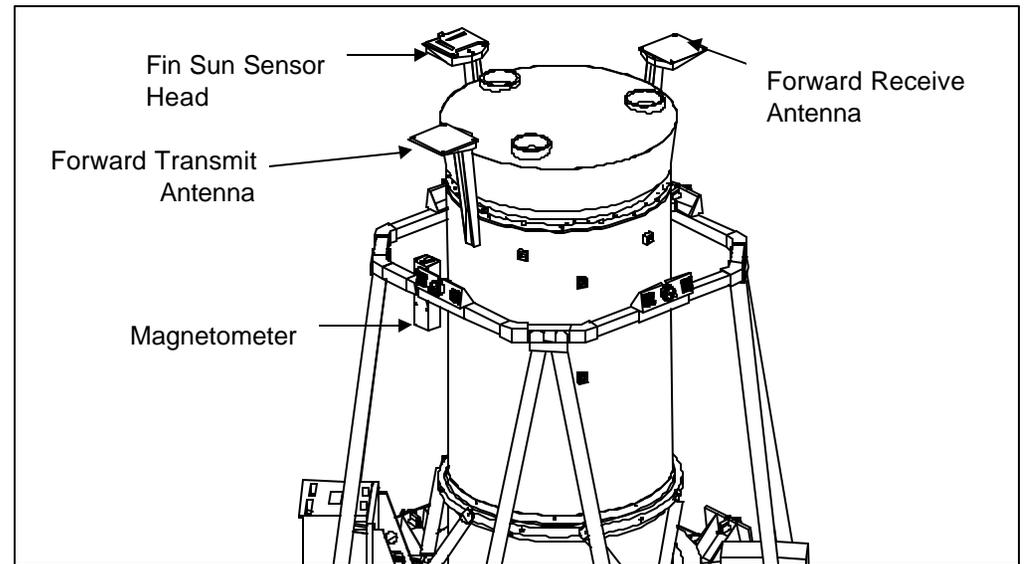
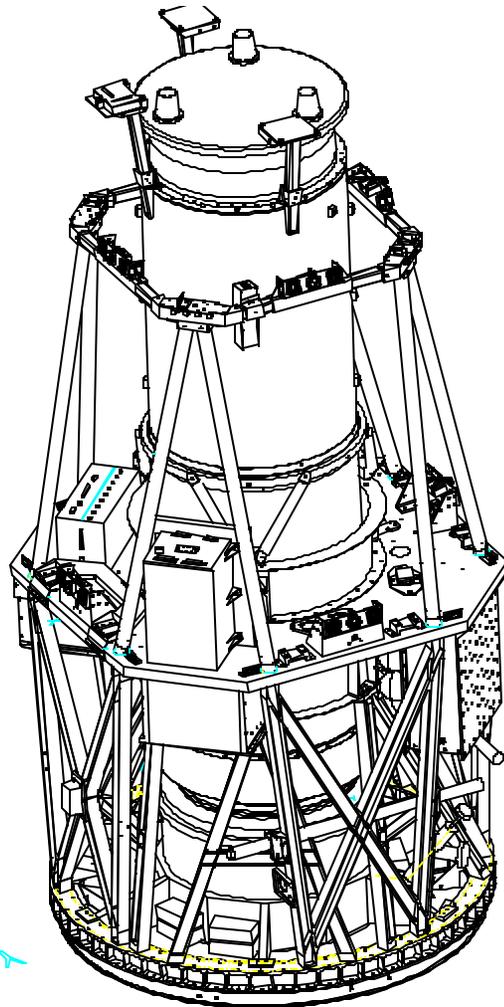
High Energy Solar Spectroscopic Imager

## COMPONENT LAYOUT



SPECTRUMASTRO

Spacecraft Shown Without Solar Array Panels





### **The Thermal Design is Cold Biased to Maintain Bus Components Within Flight Allowable Temperatures**

- Design Emphasizes Passive Thermal Design Techniques
- Only Active Design Feature is Heaters on Thermostatic Control
- Waste Heat Rejected by Radiators
  - Silver Coated Teflon Used as Radiator Coating
  - Stable Material That Minimizes Differences Between BOL & EOL Environments and Degradation
- Non-Radiator Surfaces Are Blanketed
  - 15 Layer MLI Blankets to Reduce Heat Losses from Unheated Portions of the Bus
  - 2 Mil, ITO Coated, Second Surface Kapton Outer Layer

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## THERMAL DESIGN OVERVIEW



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High Energy Solar Spectroscopic Imager

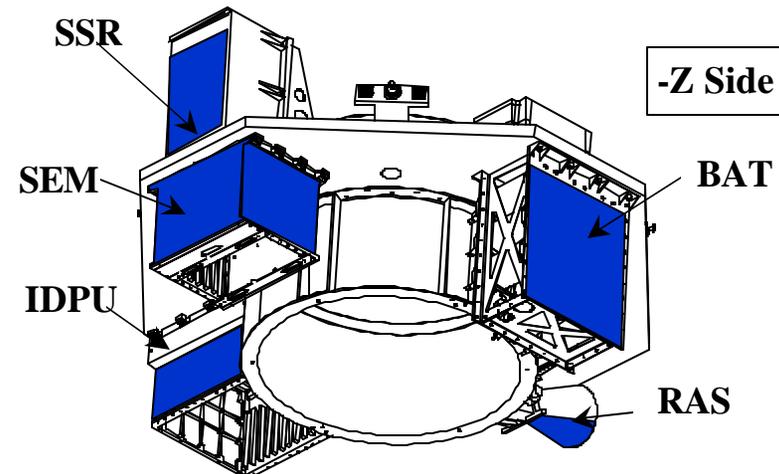
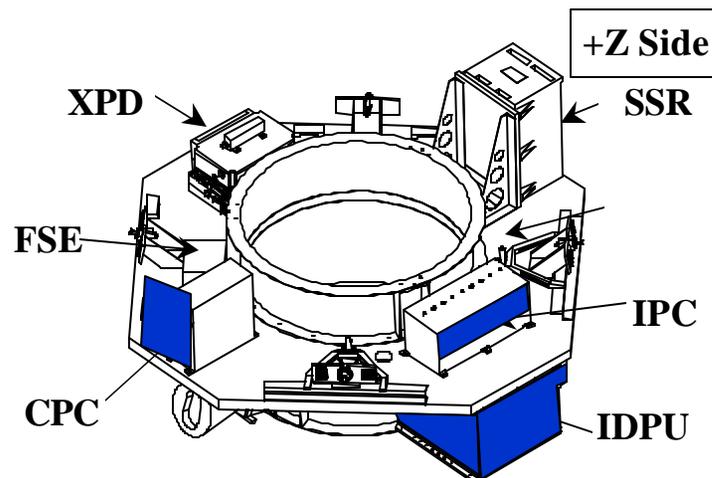
### RADIATORS

- Reject Waste Heat From S/C
- 10 mil Thick Silver Coated Teflon Tape

All Remaining Surfaces Covered With MLI Blankets



■ Denotes Radiator Locations





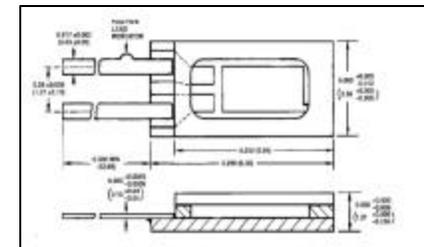
### Summary of Temperature Sensors, Thermostats and Heaters

Item #	Description	Total Ft. Qty	Part Number	Distributor	Manufacturer	Comments
<b>Temperature Sensors</b>						
1	Temperature Sensors	7	118MK2000A	Rosemount Inc	Rosemount Inc	Solar Arrays & Batteries
2	Temperature Sensors	17	AD590MF/883B	Avnet	Analog Devices	All Remaining Locations
<b>Coatings</b>						
1	Tape, ITO Coated, Silvered Teflon, 10 mil	1 Rolls	147449-002	Sheldahl	Sheldahl	
2	Tape, Aluminized Kapton, 2 mil	AR				Kapton Facing Outward
3	Tape, Aluminum Foil, 2 mil	AR				
<b>Thermostats</b>						
1	Thermostats, Battery	4	3200-2-437	Elmwood Sensors	Elmwood Sensors	Close: -5.0°C, Open: 0.0°C
2	Thermostats, SEM & SSR	2	3200-2-484	Elmwood Sensors	Elmwood Sensors	Close: -17.0°C, Open: 11.4°C
3	Thermostats, FSE & XPD	2	3200-1-46	Elmwood Sensors	Elmwood Sensors	Close: -12.0°C, Open: -2.0°C
4	Thermostats, Torque Rods	3	3200-2-525	Elmwood Sensors	Elmwood Sensors	Close: -30.0°C, Open: -25.0°C
5	Thermostats, Dampers	8	3200-2-343	Elmwood Sensors	Elmwood Sensors	Close: 11.1°C, Open: 16.1°C
<b>Heaters</b>						
1	Heater, SEM	1	642-7447-1	Tayco Engineering	Tayco Engineering	49 Ohms, Power: 15 W @ 27 VDC
2	Heaters, XPD & SSR	2	642-7447-2	Tayco Engineering	Tayco Engineering	60 Ohms, Power: 12 W @ 27 VDC
3	FSE Heater	1	642-7447-3	Tayco Engineering	Tayco Engineering	90 Ohms, Power: 8 W @ 27 VDC
4	Torque Rod Heaters	3	642-7447-4	Tayco Engineering	Tayco Engineering	242 Ohms, Power: 3 W @ 27 VDC
5	Damper Heaters	8	642-7447-5	Tayco Engineering	Tayco Engineering	1460 Ohms, Power: 0.5 W @ 27 VDC
6	Heaters, Battery (Supplied by Battery)	21				xx Ohms, Power: xx W @ 27 VDC
<b>Adhesives</b>						
1	RTV 566	2 Kits, AR				Thermal Interface Material
2	Eccobond 285	AR				Thermally Conductive Adhesive
3	EA9394	AR				Structural Adhesive (Tstats & Solar Array Temperature Sensors)

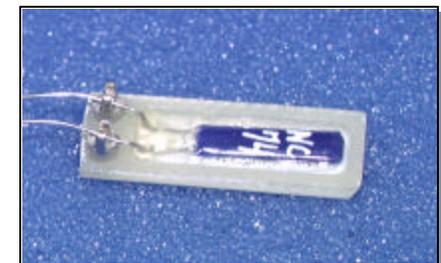
### Summary of Temperature Sensors That are Processed by the S/C PACI Board

Description	Mounting Location	Part Number	AD590 Analog #	Hardware Telemetry List Function Number
Solar Array 1 - Back	Inboard Panel	118MK2000A		004-006-004
Solar Array 2 - Back	Inboard Panel	118MK2000A		004-006-005
Solar Array 3 - Back	Inboard Panel	118MK2000A		004-006-006
Solar Array 4 - Back	Inboard Panel	118MK2000A		004-006-007
Battery, Chassis	Chassis, Near Interface	118MK2000A		004-006-008
Battery, Cell Temp 1	On Cold Cell	118MK2000A		004-066-021
Battery, Cell Temp 2	On Hot Cell	118MK2000A		004-066-022
Fine Sun Sensor Elec	Housing, External	AD590MF/883B	01	006-007-006
IAD, X	Mounting Bracket	AD590MF/883B	02	001-006-003
IAD, Y	Mounting Bracket	AD590MF/883B	03	001-006-004
SEM, Chassis	Housing, External	AD590MF/883B	04	006-006-010
SEM, DC-DC Converter	Converter Case, External	AD590MF/883B	05	006-006-011
SEM, OCXO Precision Clock	Clock Case, External	AD590MF/883B	06	006-006-012
Solid State Recorder	Housing, External	AD590MF/883B	07	006-006-009
Torque Rod, X	TSTAT Bracket on Torque Rod	AD590MF/883B	08	006-007-013
Torque Rod, Y	TSTAT Bracket on Torque Rod	AD590MF/883B	09	006-007-014
Torque Rod, Z	TSTAT Bracket on Torque Rod	AD590MF/883B	10	006-007-015
Transponder, Housing	Housing, External	AD590MF/883B	11	002-006-004
Transponder, Power Amp	Internal	N/A		002-006-009
Transponder, Power Supply	Internal	N/A		002-006-010
MLI	Deck, Outer Layer, Sun Side	AD590MF/883B	12	001-006-005
IDPU	Housing, External	AD590MF/883B	13	006-021-001
IPC	Housing, External	AD590MF/883B	14	006-021-003
CPC	Housing, External	AD590MF/883B	15	006-021-002
Spectrometer	Base Ring, +Z Side	AD590MF/883B	16	006-021-015
RAS	Housing, External	AD590MF/883B	17	006-021-004
Particle Detector	Housing, External	AD590MF/883B		Not Assigned Yet
Imager	Mounting Interface, Near Battery Mount	AD590MF/883B		Not Assigned Yet
Magnetometer, Housing	Mounting Bracket	AD590MF/883B		Not Assigned Yet
Magnetometer	Internal	N/A		006-007-012

### AD590



### PRT





### “Snap” of Flight Ops Display Window Showing the PACI Temperature Sensor Telemetry

S/C Bus Temperature Sensors

ANALOG INS		ANALOG INS		AVERAGED AD590s		PRTs		DIGITAL INS	
FSS SINE1:	-0.001	BAT CURRENT:	7.138	FSE TEMP:	-9.355	BATT TEMP:	17.876	XMIT SWITCH:	FWD
FSS COS1:	-0.011	BAT MIDVOLT:	14.035	IAD1 TEMP:	-12.118	S/A WING 1:	127.237	XMIT POWER:	OFF
MAG X:	-2.446	BATT TEMP 1:	16.380	IAD2 TEMP:	-8.270	S/A WING 2:	127.237	RCV SUBLOCK:	NO_LOCK
MAG Y:	0.643	BATT TEMP 2:	18.583	SEM TEMP:	0.998	S/A WING 3:	127.237	RCV CARLOCK:	NO_LOCK
MAG Z:	0.707	BAT VOLTAGE:	30.979	DC/DC TEMP:	6.865	S/A WING 4:	127.237	FSE SUNPRES:	NOT_IN_SUN
BATT PRESS1:	4525.07	CCB XSISTOR:	9.984	DCXD TEMP:	2.083	IAD1 POSN:	-4.642	SSR CMDRDY:	READY
BATT PRESS2:	4371.54	SA CURRENT:	15.337	SSR TEMP:	-0.382	IAD2 POSN:	-3.602	CCB MSMODE2:	MISSION
RCVR STRESS:	0.048	VT CURVE:	6.898	TRQX TEMP:	-29.469	SPARE PRT:	9.984	CCB MSMODE1:	MISSION
RCV STRNGTH:	1.039	ESSBUS CUR:	0.636	TRQY TEMP:	-25.032			PCB IDPUPWR:	ON
XMT VOLTAGE:	5.006	ESSBUS -15V:	-15.146	TRQZ TEMP:	-27.103			PCB OC TRIP:	OK
XMT PWRAMPT:	-8.324	HESSI BUSCUR:	0.323	XPNDR TEMP:	-9.601	CHANNEL 1:	2.946	PCB UV TRIP:	OK
XMT PWRSPYT:	-7.766	IPDU HTRBUS:	0.001	DECK TEMP:	-8.960	CHANNEL 2:	2.946	CPU PWR STS:	ON
XMT RE PWR:	-0.001	NEB2 BUSCUR:	0.050	IDPU TEMP:	-8.911	CHANNEL 3:	2.946	CCB TEMSEL:	A
MAG TEMP:	-15.076	IDPU CURR:	1.339	IPC TEMP:	-0.579	CHANNEL 4:	2.946		
FSE SUH:	3.522	CRYO CURR:	4.151	CPC TEMP:	-0.431	CHANNEL 5:	2.946	FSS DIG 1:	0000
SSR +5V:	3.654	IDPU LD CUR:	0.802	SPEC TEMP:	-23.849	CHANNEL 6:	2.946	FSS DIG 2:	0001
SSR +3.3V:	2.278	TROD X CURR:	-0.230	RAS TEMP:	-29.568	CHANNEL 7:	2.946	FSS DIG 1:	0
ESS +5V:	4.991	TROD Y CURR:	-0.221			CHANNEL 8:	2.946	FSS DIG 2:	1
CPU +5V:	5.045	TROD Z CURR:	0.224					Chn1s 0-15:	FFE3
TROD Z (RED):	0.000	ESSBUS +15V:	14.953	SPARE TMP1:	-49.485			Chn1s 16-31:	FE08
				SPARE TMP2:	-49.485				
				SPARE TMP3:	-49.485				



### Heater Switches

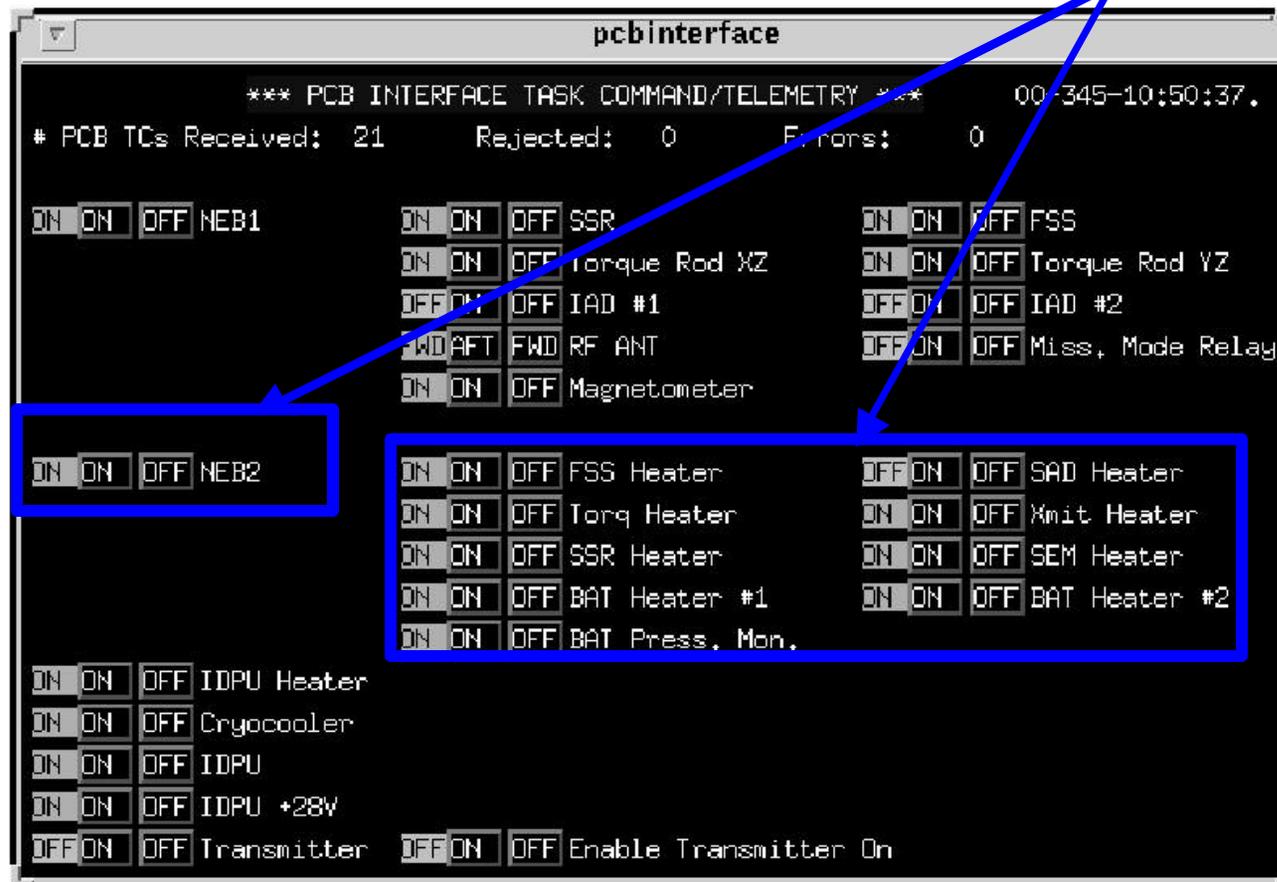
- Switches Merely Enable or Disable Heater Circuit (I.e, Does not Indicate Whether or Not the Heater is Actually Drawing Power)
- Nominally All S/C Bus Heaters Should Be Enabled at All Times. The Only exception is the Damper Heaters (i.e., SAD), which Should Be Disabled/Turned OFF Once it has Been Verified That the Solar Arrays Have Been Fully Deployed
- S/C Bus Heaters are Thermostatically Controlled. Heaters Automatically Turn ON When Temperature Drops Below the “Close” Set Point and Turn OFF When Temperature Rises Above the “OPEN” Set Point). Note: All Bus Thermostats Have Fixed Set Points (i.e, They are Not Programmable.)

### Heater Circuit Telemetry

- For the S/C Bus Heater Circuits, There is no Direct Indication of the Whether or not the Heater Circuit is Actually Drawing Power. Must Rely on Other Data to Determine Whether or not a Heater Circuit is Drawing Power. Specifically, a Change in Temperature or Change in the Current (Amps) are Good Indicators.
- UCB Components, However, are Equipped With Telemetry to Directly Indicate Heater Performance and Status

### “Snap” of the Heater Switch Flight Ops Display Window

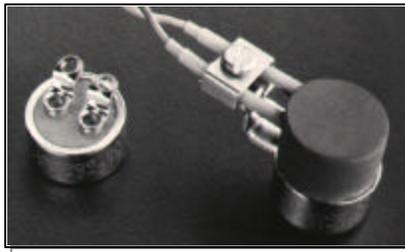
S/C Bus Heater Switches



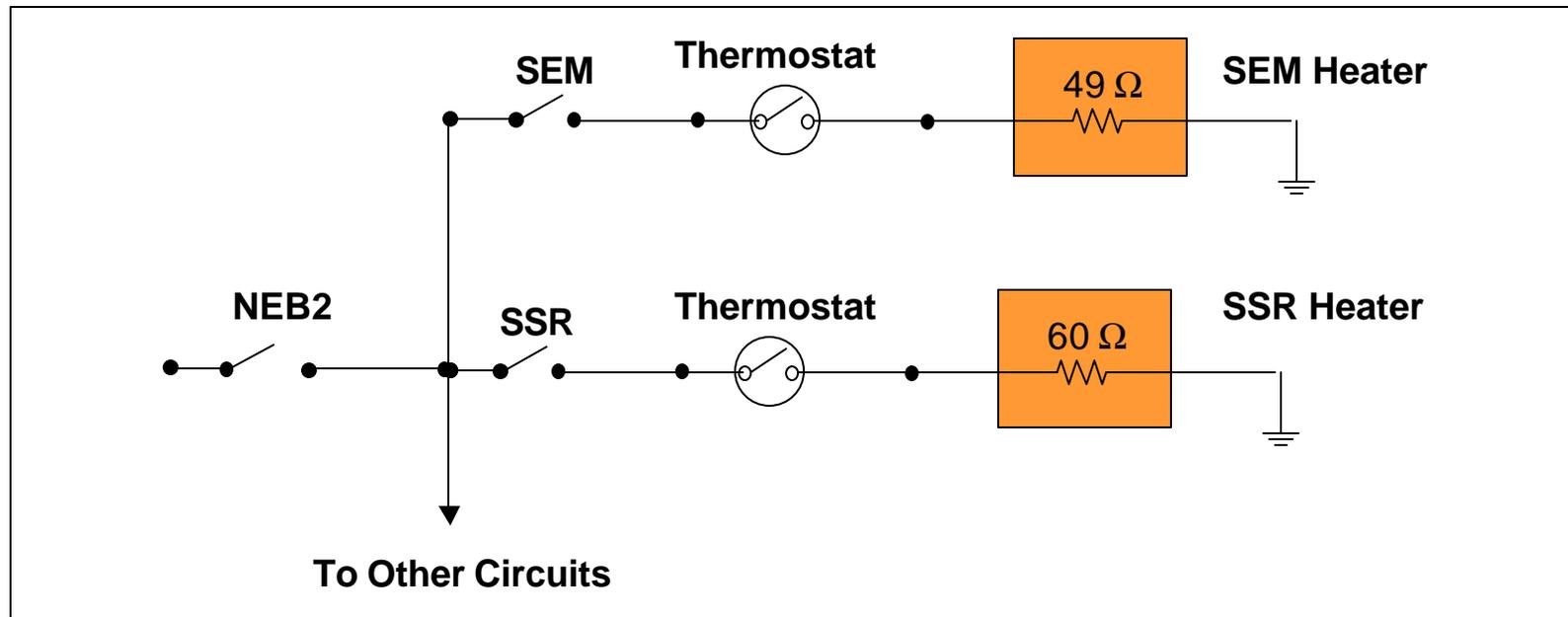


### S/C Bus Heater Circuits are Thermostatically Controlled

Thermostats



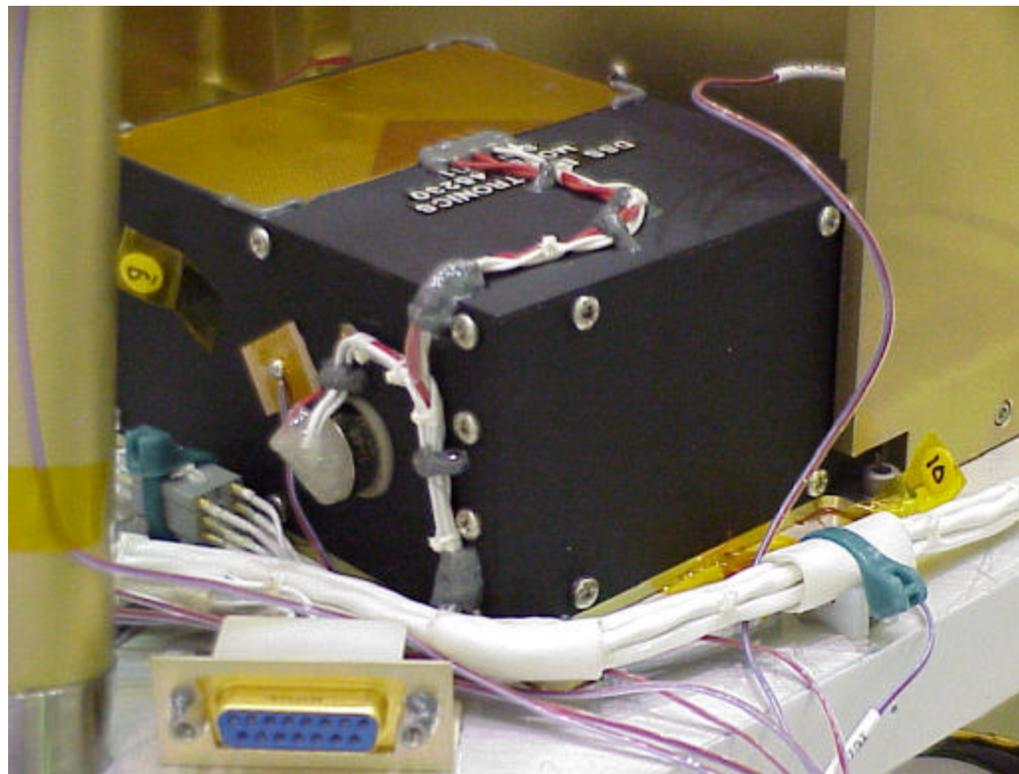
Heaters



### Example of Actual Heater, Temperature Sensor and Thermostat Locations

- More Pictures of Other Bus Thermal Hardware Contained in Appendix A

### Fine Sun Sensor Electronics





**In Addition to the S/C Bus, the UCB Instrument Components are Equipped with Several Heater Circuits.**

### **Summary of UCB Instrument Heater Circuits**

- Deck-Mounted Components
  - CPC, IPC, IDPU, RAS
- Imager
  - Upper grid tray heaters
  - Lower Grid Tray Heaters
- Spectrometer
  - Cryocooler Collar
  - Detector Cold Plates



### Key Operational Considerations and Constraints for S/C Bus

- Battery
  - Narrow Operating Temperature Range. Likes to Run Cold (I.e., 5C to -5C)
  - Autonomous Heaters Protect Battery from Ever getting too Cold ( < -6C)
  - At Launch Batteries Start out Warm (10C to 15C). There are basically two Options for
  - VT Curve Selection Affects Battery Temperature (Lower VT => Lower Temp)
- Transponder
  - Designed to Accommodate up to Two (2) 15-Minute Downlinks per Orbit. Once the On-Orbit Operation has Been Characterized, It may be possible to Perform More Than Two Downlinks if Temperature Permits.
  - During Downlinks, Temperature May Increase Rather Quickly by as much as 8C. Consequently, the Transponder Should Have at a minimum 8C margin to the Yellow Limits.



### Possible Options for Component(s) Exceeding Hot Limits:

- Put Component or Neighboring Components Into a Lower Power Mode
- Turn Component or Neighboring Components OFF
- Disable Heater (Thermostat may have Failed in the ON Position)
- Lower V/T Curve (For Battery Only)
- Shut Off Transmitter
- Limit Transponder Downlink/Transmitting Operations (I.e., Decrease or eliminate the number of Downlinks per Orbit)



### Possible Options for Component(s) Exceeding Cold Limits:

- Put Component or Neighboring Components Into a Higher Power Mode
- Turn Component or Neighboring Components ON
- Enable Heater/Verify Heater Enabled
- (Thermostat may have Failed in the OFF or Position)
- Raise V/T Curve Selection (For Battery Only)
- Turn ON Transmitter
- Increase Transponder Downlink/Transmitting Operations



### Summary

- **Transponder Temperatures Increase Rapidly and Significantly During Downlinks (i.e., Transmitting). Should Adhere to the 2 Downlinks per Orbit Design Limit.**
- **Battery Temperatures Rise When Discharging and When Overcharging. Should the Battery Experience Significant Overcharging, Lowering the VT Curve Selection Should Alleviate the Problem.**
- **Since the S/C is Continuously Pointed at the Sun and Rolling, the Thermal Environment that HESSI is Subjected to is Relatively Stable . Consequently, the Temperatures Should be stable as well.**
  - Nominally, Sinusoidal Temperature Variations Will Occur Due to the Cyclical Transitions Between Shadow and Sunlight, Repeating Approximately Every 95 Minutes
  - In Addition, Seasonal Changes Will Slightly Alter the Thermal Environment, Which in Turn Will Cause Minor Shifts in Temperatures
- **Highly Recommend becoming Familiar With the Component “Yellow” and “Red” Temperature Limits, which are Listed by Mnemonic on the HESSI Website.**

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## APPENDIX A



High Energy Solar Spectroscopic Imager

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### Pictures of Heaters, Thermostats and Temperature Sensors



### Typical Temperature Profiles

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High Energy Solar Spectroscopic Imager

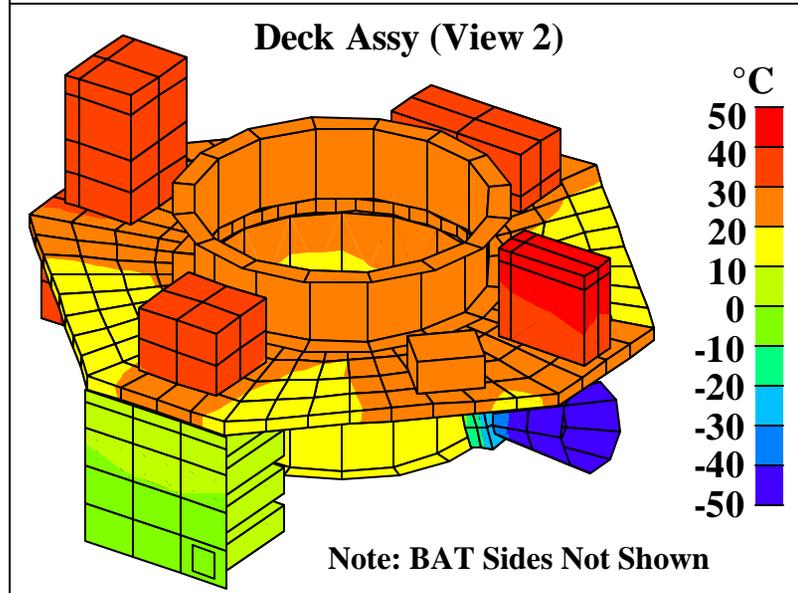
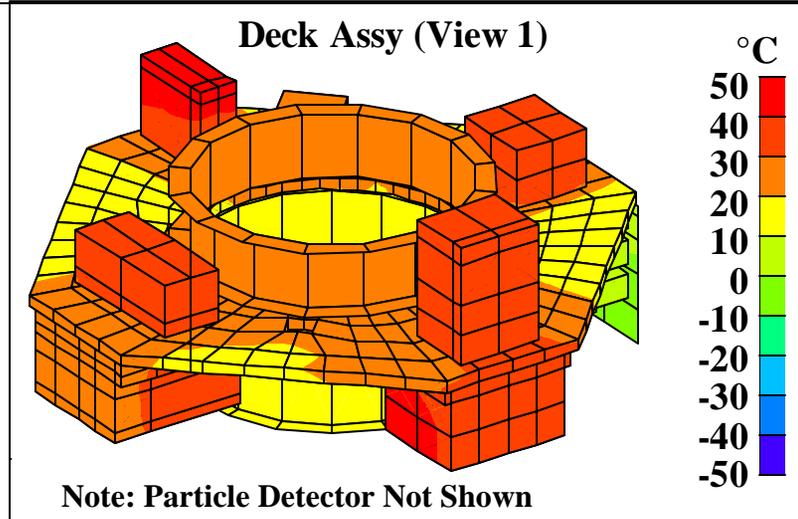
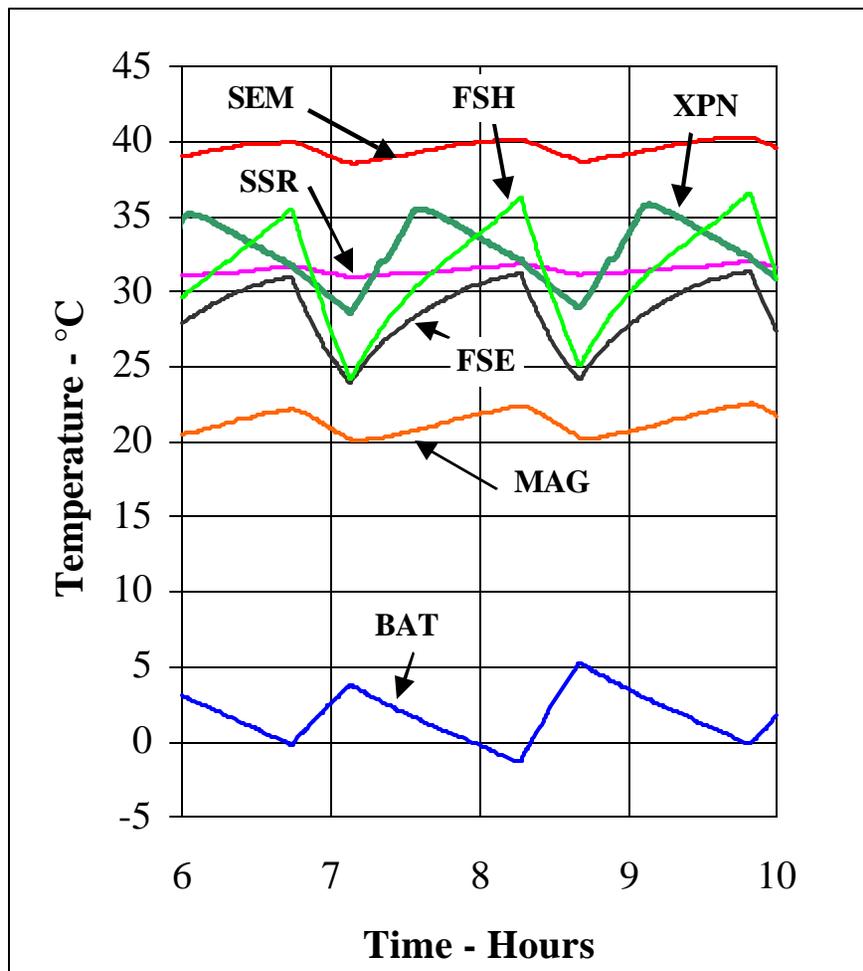
## COMPONENTS ON DECK



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### THERMAL CONTROL SUBSYSTEM HOT CASE MODEL RESULTS, $\beta = 61^\circ$





### THERMAL CONTROL SUBSYSTEM COLD CASE MODEL RESULTS, $\alpha = 0^\circ$

