Decouple each IC
Locate near pin 9 of U24
Spare OP-AMP
Locate near pin 8 of U24
NOTE: C58 is marked wrong on silkscreen of Rev. A and Rev. B DC Boards. It should be installed "backwards" with respect to the silkscreen polarity markings.
NOTE: PULSE SIGNALS SHOWN ARE NOT TO SAME TIME SCALE.
CLOCK TERMINATION NETWORKS

Place at end of clock nets

Title: DC Board - Oscillator

Date: Friday, March 19, 1999

Sheet 9 of 16
NOTE: Pins 1 and 11 of U30 are incorrect in the DCB REV. B layout. Pin 1 is connected to VCC and pin 11 to GND in the artwork. Rework must be performed in order to connect Pin 1 to GND and Pin 11 to VCC as shown here.

Data Controller Board - Backplane Interface

Title: [Data Controller Board - Backplane Interface]

Date: Friday, March 19, 1999

Document Number: [Doc]

Rev: [C]

Sheet: [B]

Dimensions: 1224.0x792.0

Sheet: 1

Page: 1

Pin assignments and connections are shown in the diagram.